

A

B

C

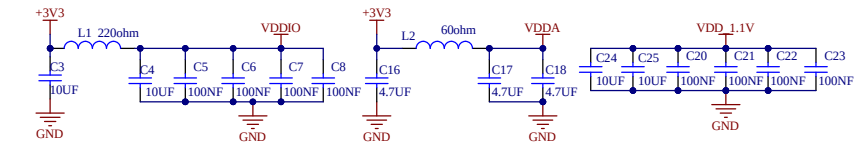
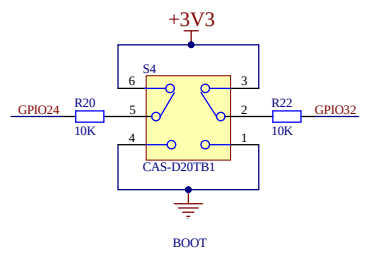
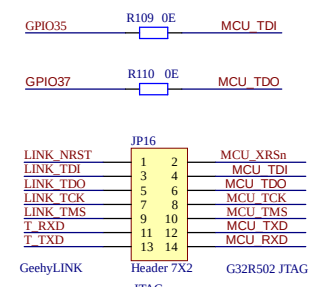
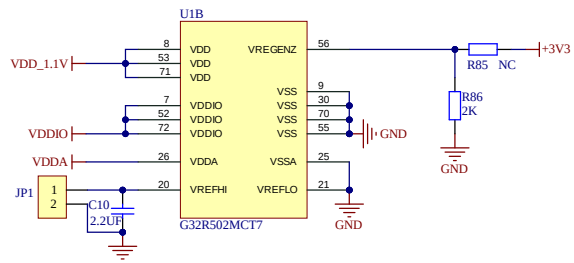
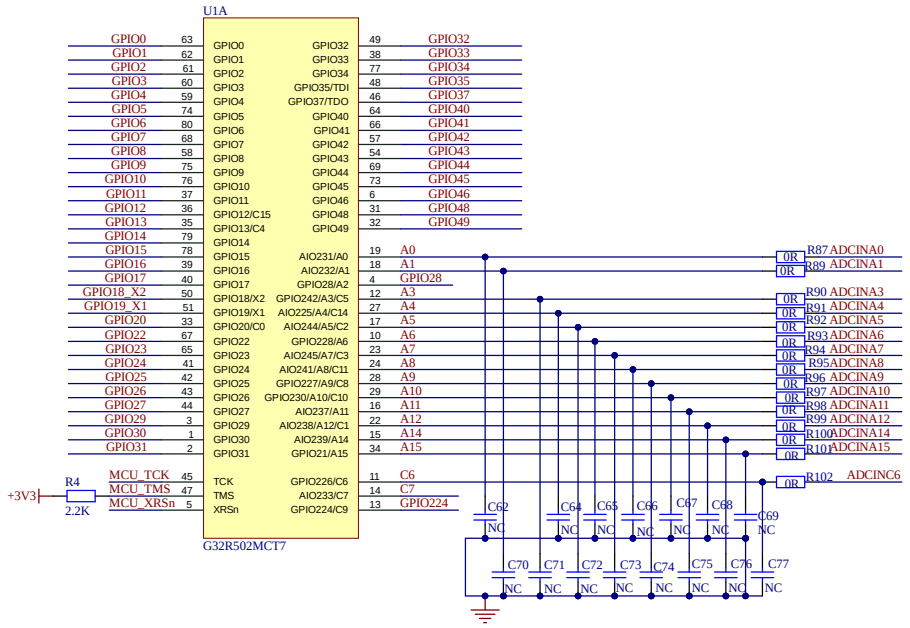
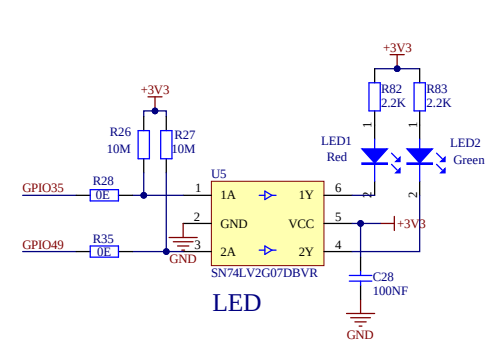
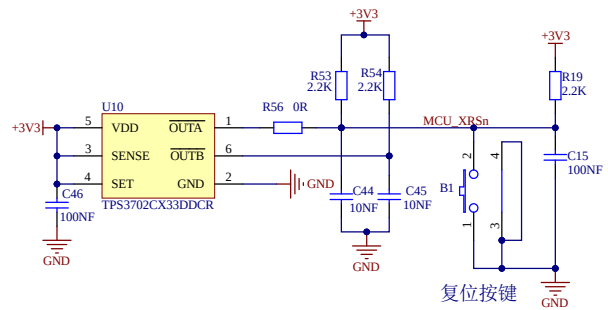
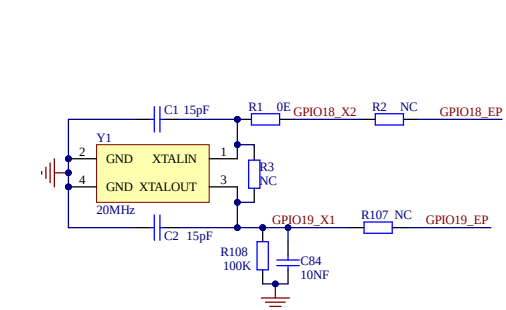
D

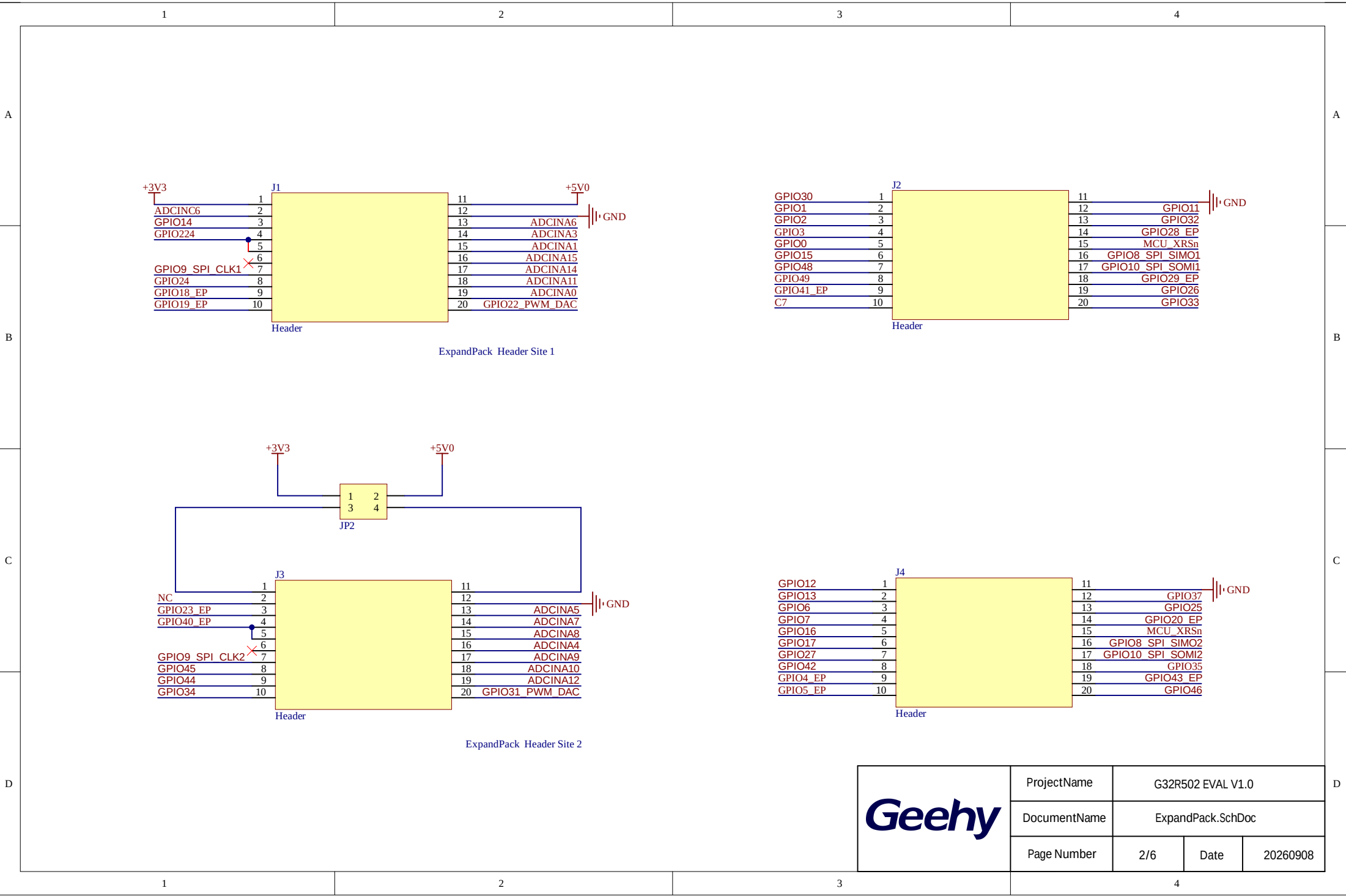
A

B

C

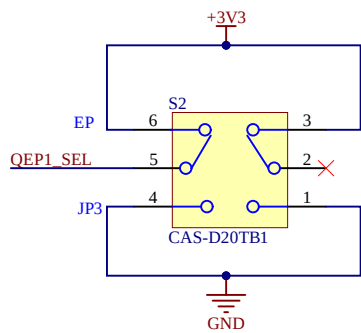
D



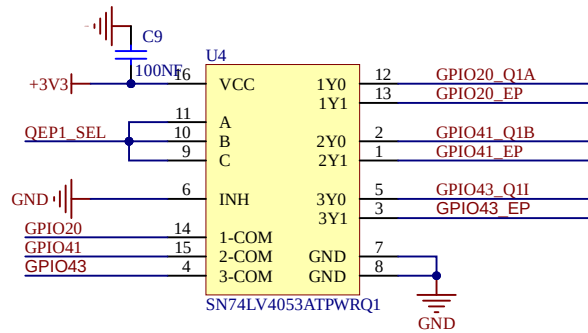


Geehy

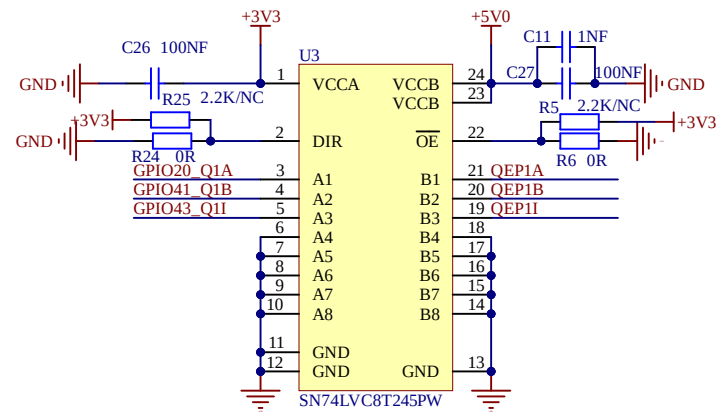
ProjectName	G32R502 EVAL V1.0		
DocumentName	ExpandPack.SchDoc		
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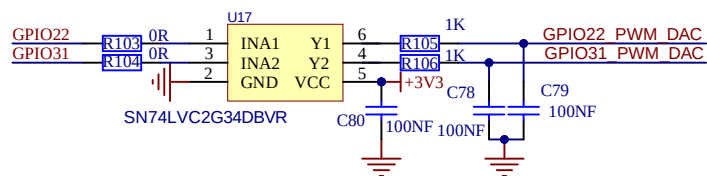
信号选择



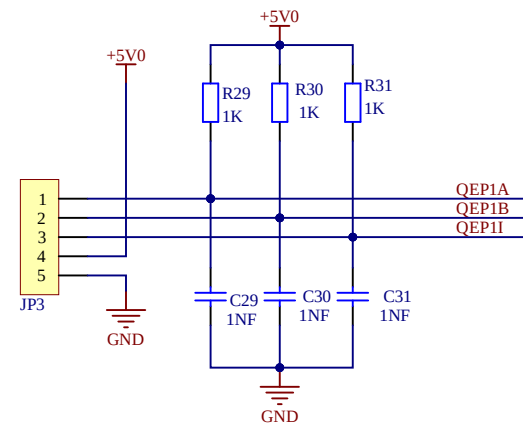
信号选择



QEP电平转换



PWM-DAC

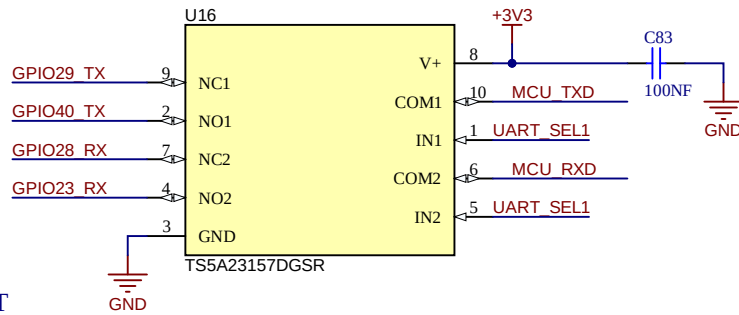
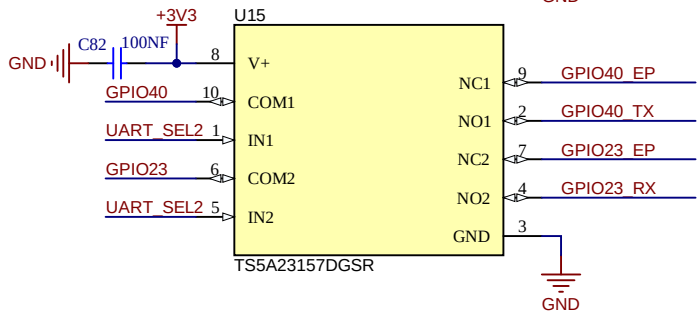
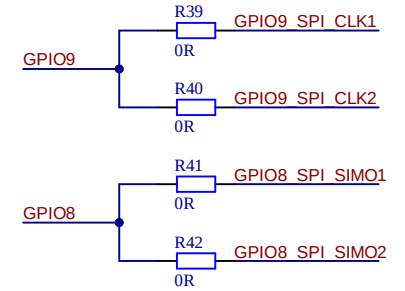
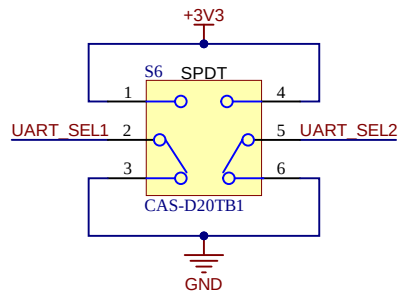
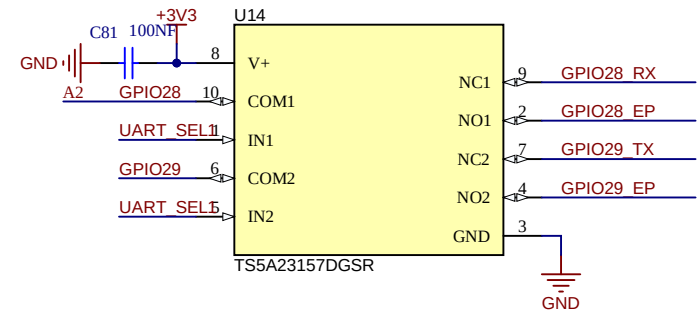


QEP

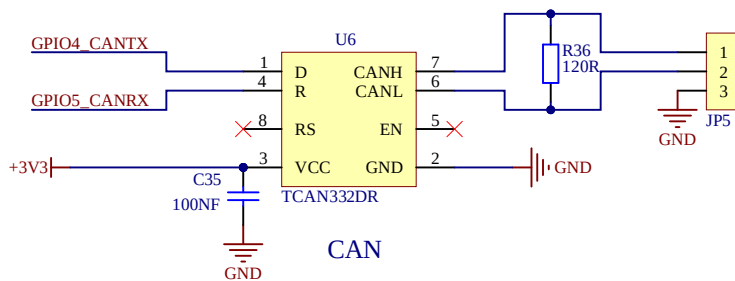
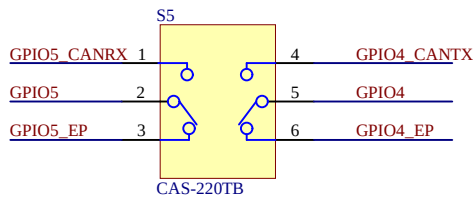
Geehy

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UART_SEL1	UART_SEL2	GPIO28/29	GPIO23/40
0	0	COM Port	EP
0	1	COM Port	NC
1	0	EP	EP
1	1	EP	COM Port



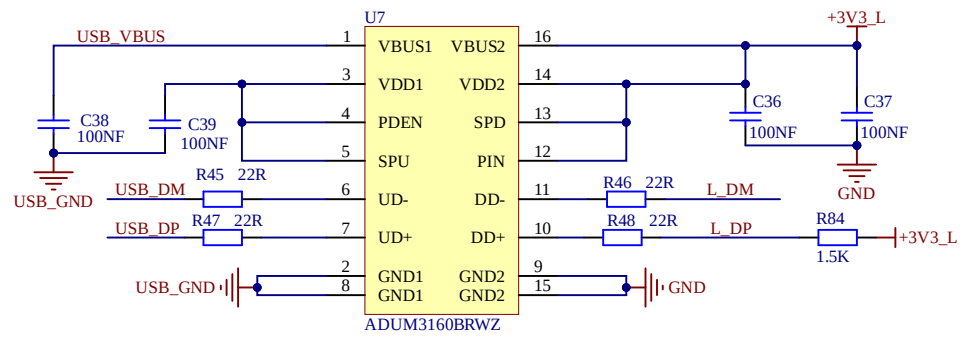
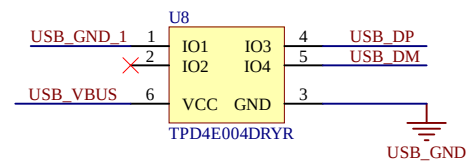
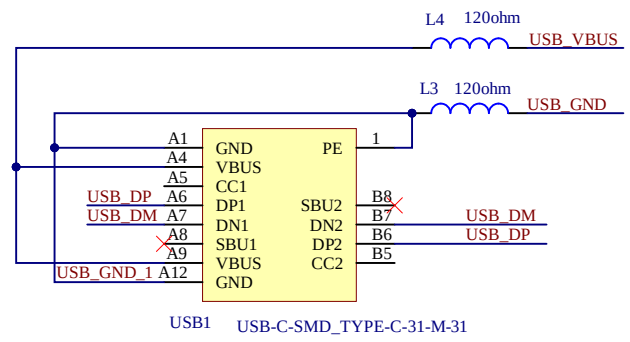
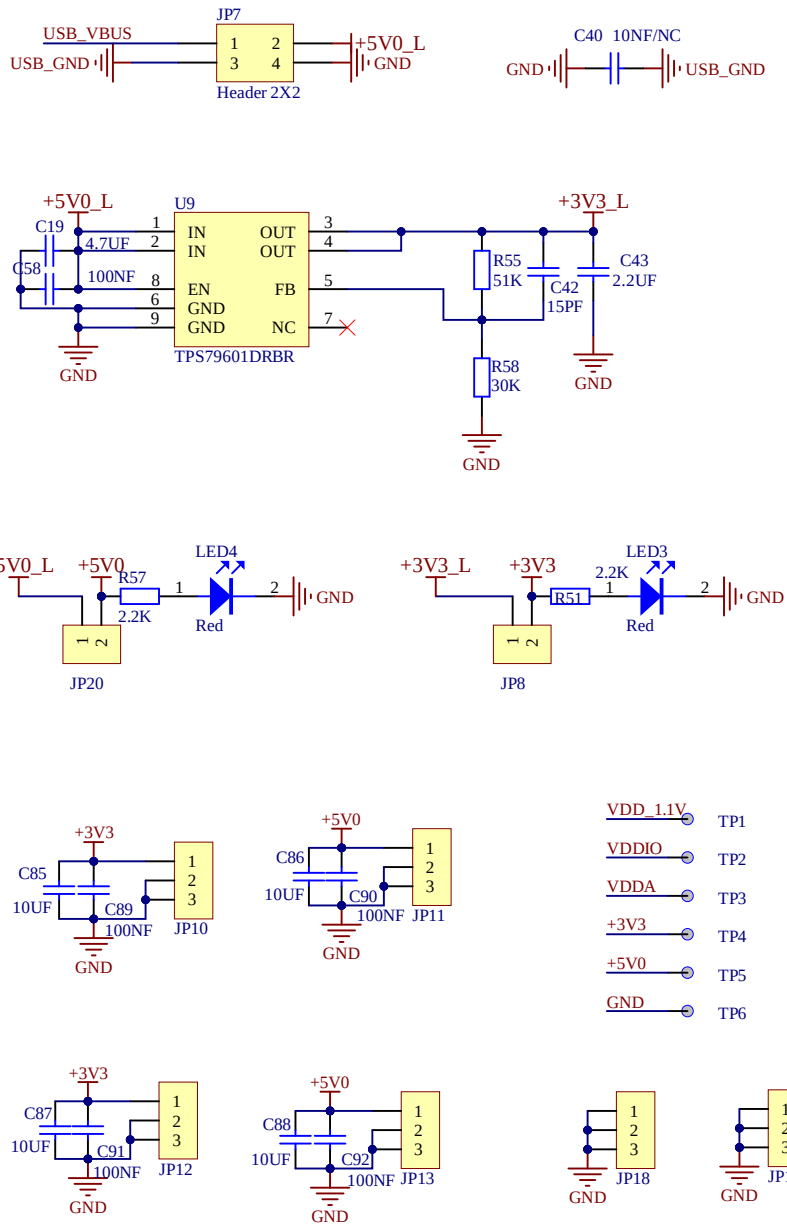
SPI

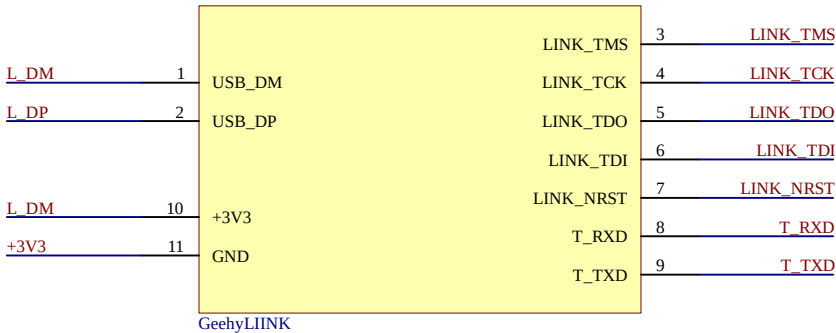


CAN

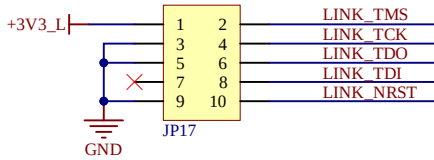
Geehy

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GeehyLINK



Geehy

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